

WHAT IS CLAIMED IS:

1. A semiconductor integrated circuit device comprising:
a plurality of internal power supply generating circuits arranged on
5 a single chip; and
a common monitor pad;
wherein the internal power supply generating circuits are connected
via respective switches to the common monitor pad, and
the internal power supply generating circuits and the monitor pad
10 are selectively connectable by the switches.
2. The semiconductor integrated circuit device according to claim 1,
wherein the internal power supply generating circuits generate equal
internal power supply voltages.
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3. The semiconductor integrated circuit device according to claim 1,
wherein each of the internal power supply generating circuits generates an
internal power supply based on an external power supply.
- 20 4. The semiconductor integrated circuit device according to claim 1,
wherein all of the switches are capable of being turned off at the same time,
and each of the switches is capable of being turned on or off selectively.
5. The semiconductor integrated circuit device according to claim 1,
25 wherein each of the switches is an N-channel transistor or a P-channel
transistor.
6. The semiconductor integrated circuit device according to claim 1,
wherein the internal power supply generating circuits are capable of being
30 all or selectively brought into a deactivated state.
7. The semiconductor integrated circuit device according to claim 6,
further comprising driver control portions connected to the internal power
supply generating circuits,
35 wherein the driver control portions control a supply of a voltage to
the internal power supply generating circuits, the voltage being input via an
external pad.

8. The semiconductor integrated circuit device according to claim 7,
wherein the driver control portions are provided respectively for the internal
power supply generating circuits, and the forcible application portions and
5 the switches are controlled by a common control signal.

9. The semiconductor integrated circuit device according to claim 1,
further comprising voltage level shifting circuits between the internal power
supply generating circuits and the switches,
10 wherein the voltage level shifting circuits shift voltage levels of
internal power supplies generated in the internal power supply generating
circuits.